



FIG. 3

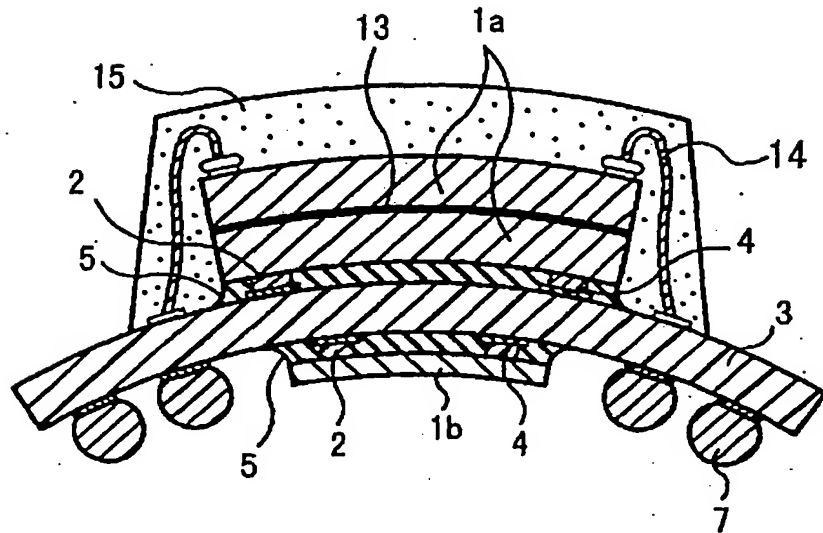


FIG. 4

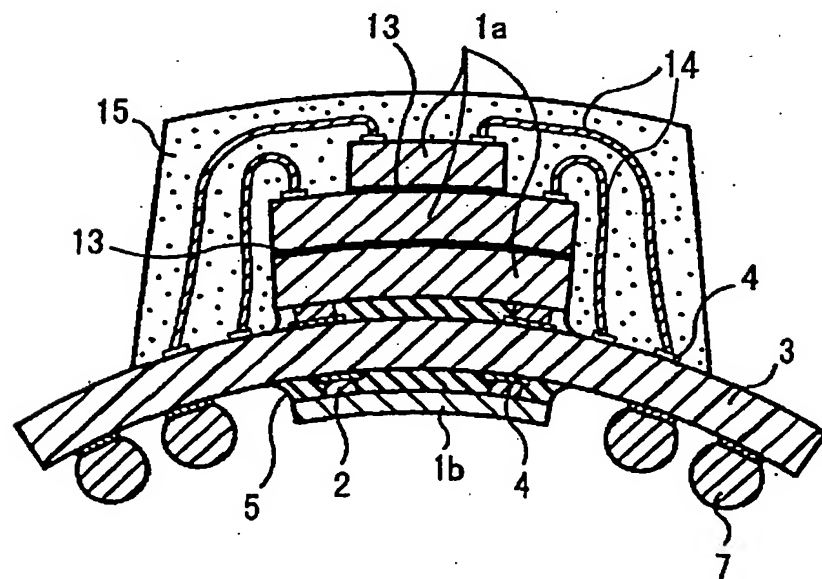


FIG. 5

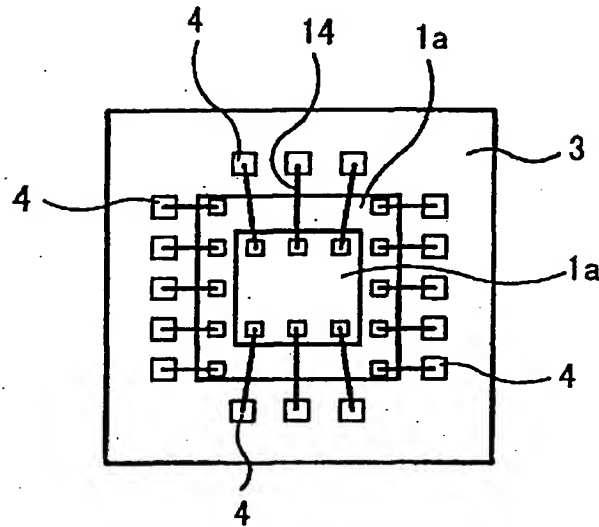


FIG. 6 A

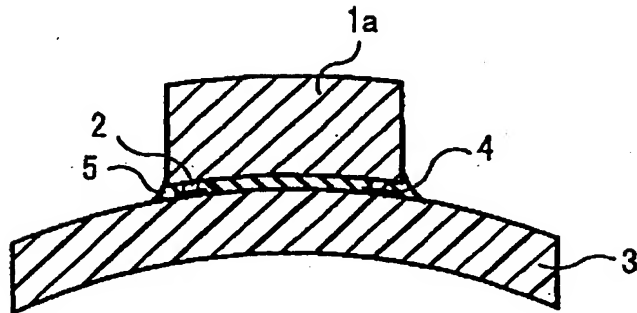


FIG. 6 B

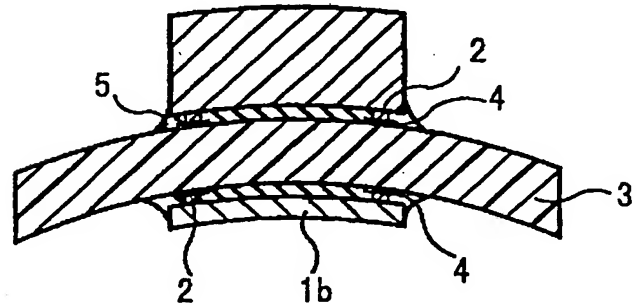


FIG. 6 C

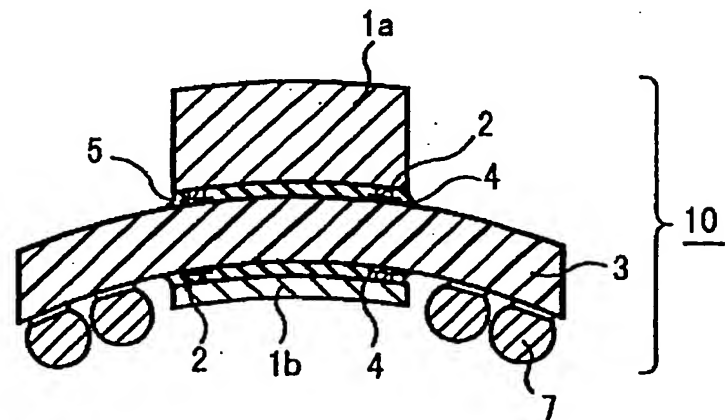


FIG. 7A

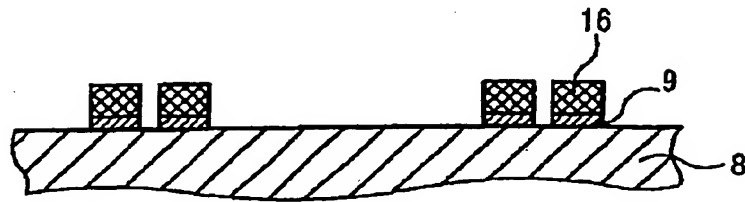


FIG. 7B

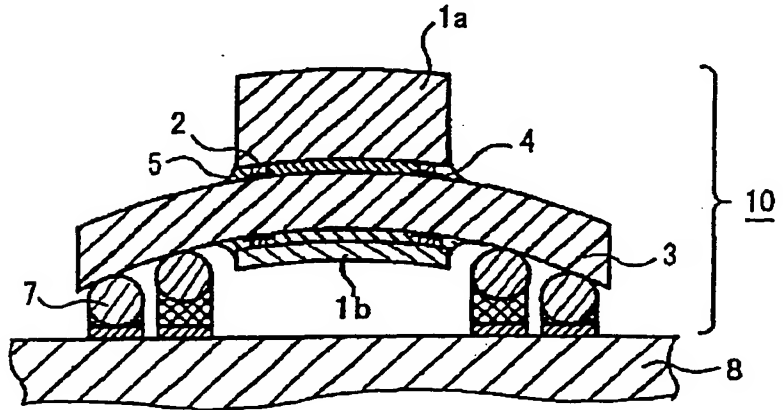


FIG. 7C

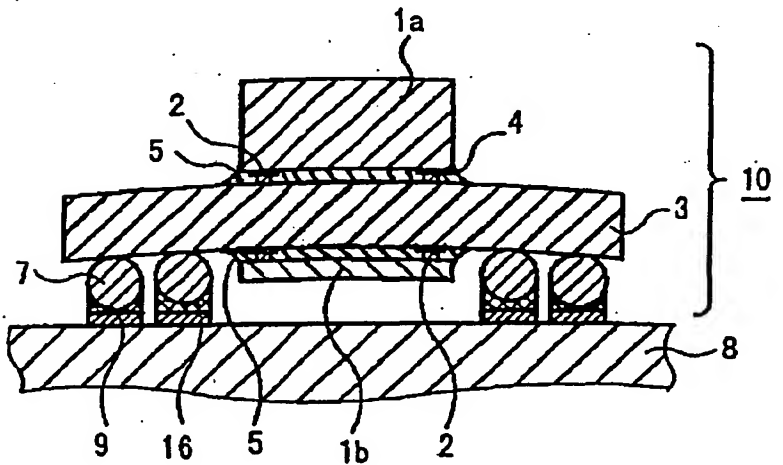


FIG. 8

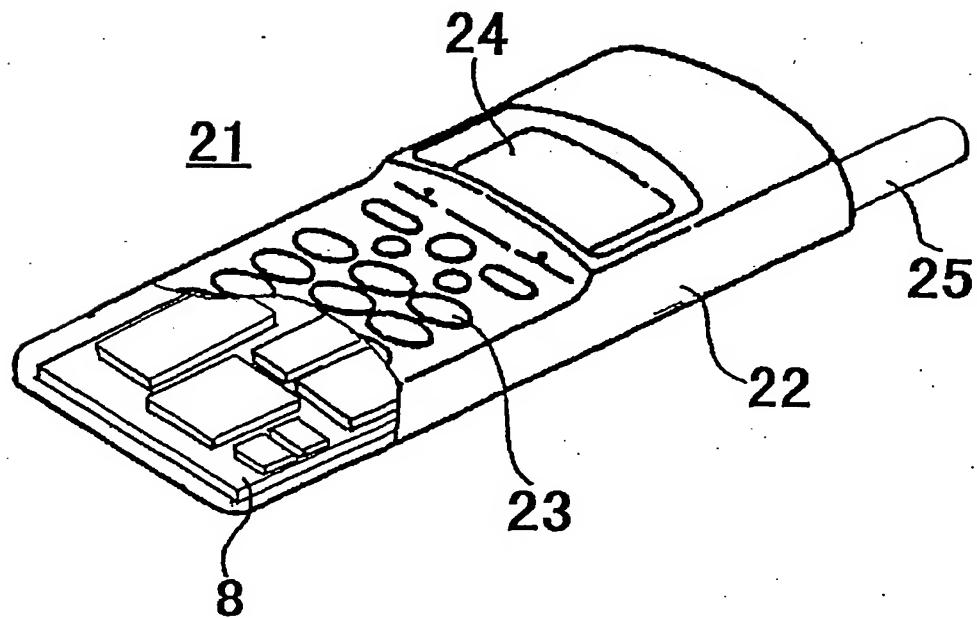


FIG. 9

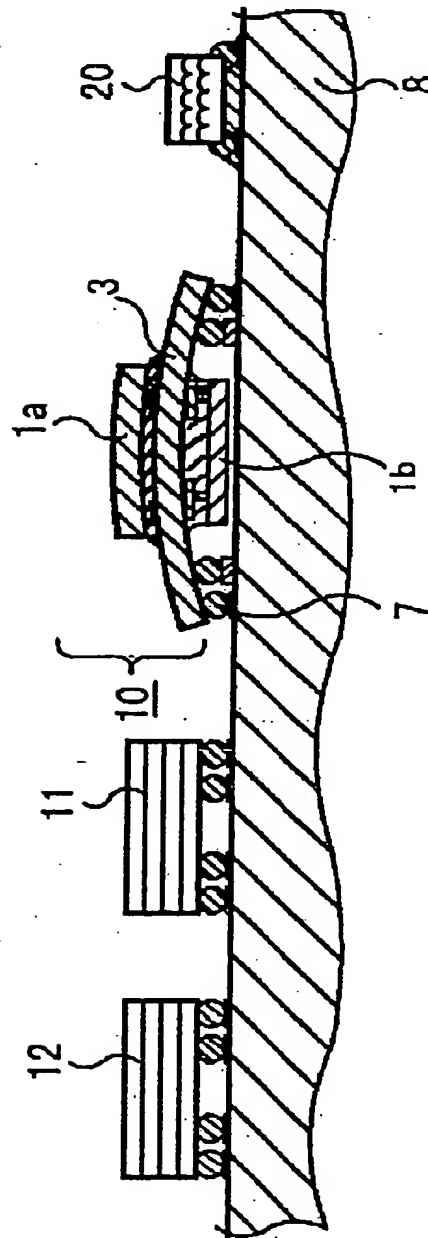
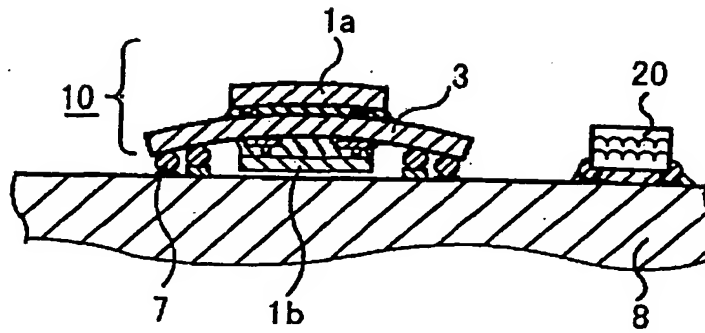


FIG. 10





This cross-sectional view shows a central channel region (102) flanked by side regions (104, 105). The channel region contains a central layer (103) and is bounded by a layer (107). The side regions (104, 105) are separated from the channel by a layer (106). The device is mounted on a substrate (108) with a top layer (109). The side regions (104, 105) are further defined by layers (116, 117) and (118, 119) respectively.

FIG. 12 PRIOR ART

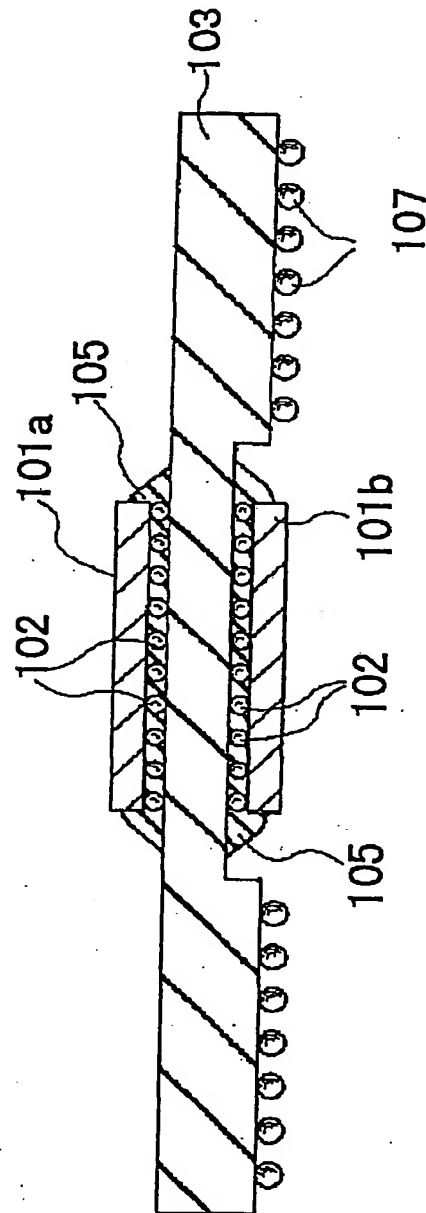


FIG. 13 PRIOR ART

